

FIG. 1

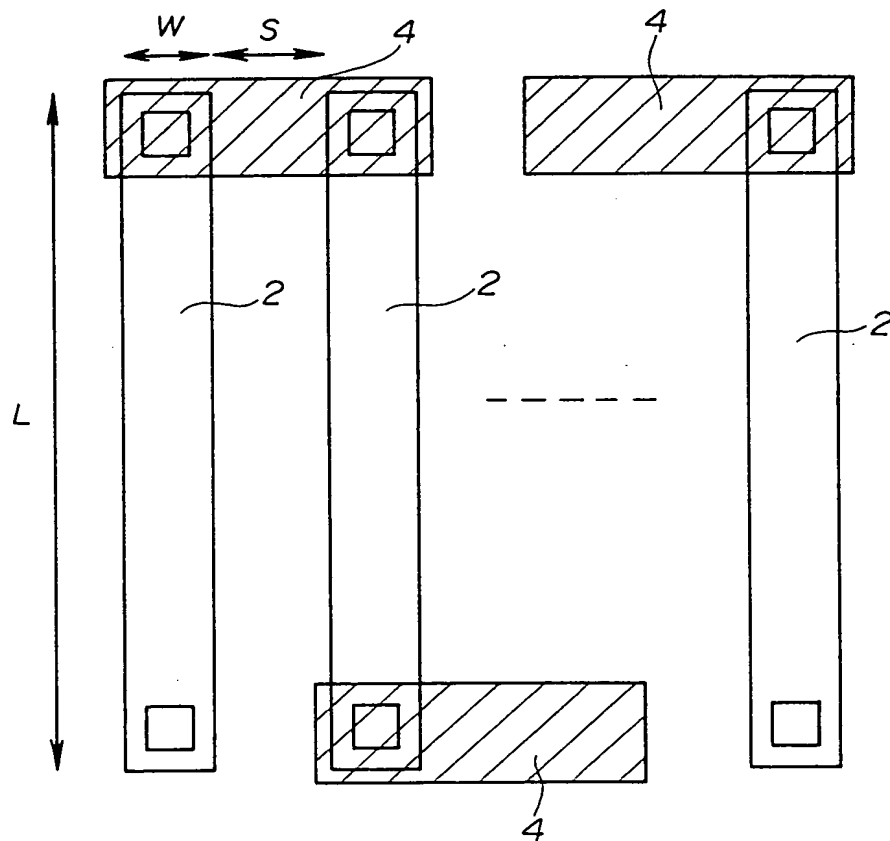


FIG.2A

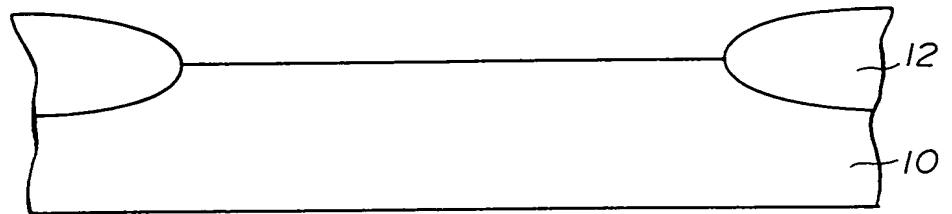


FIG.2B

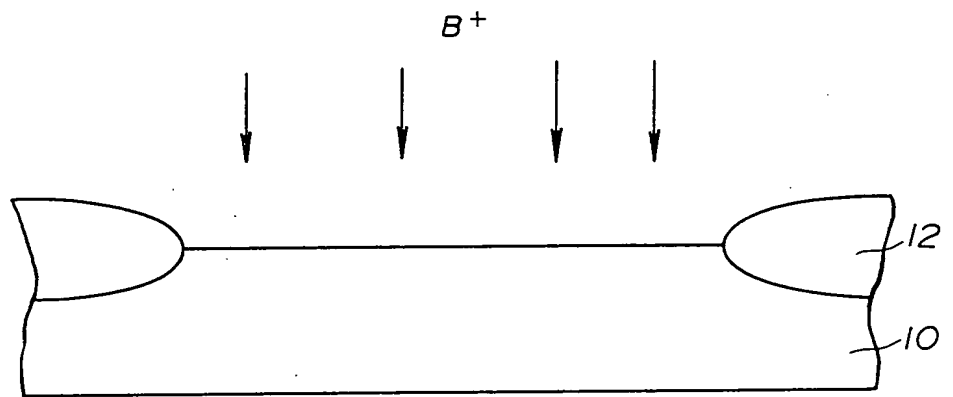


FIG.2C

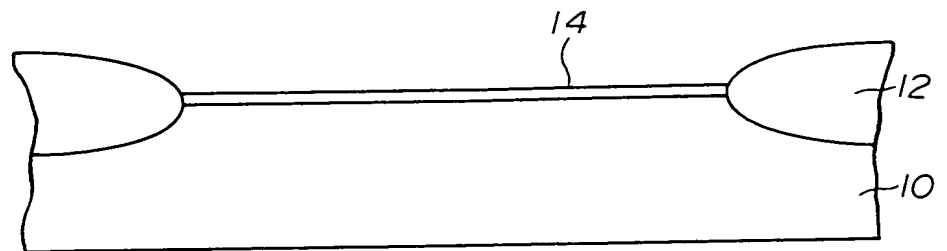


FIG. 2D

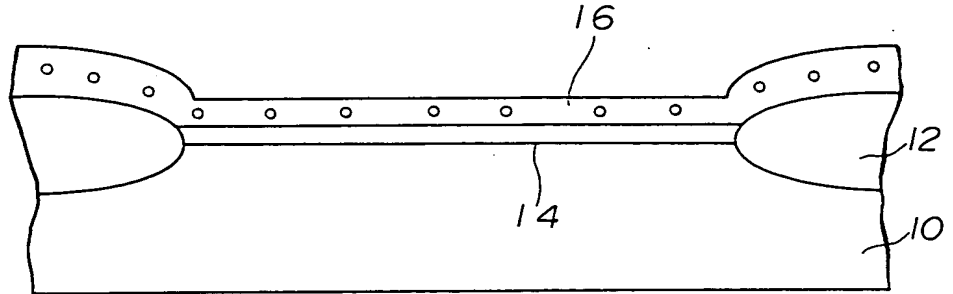
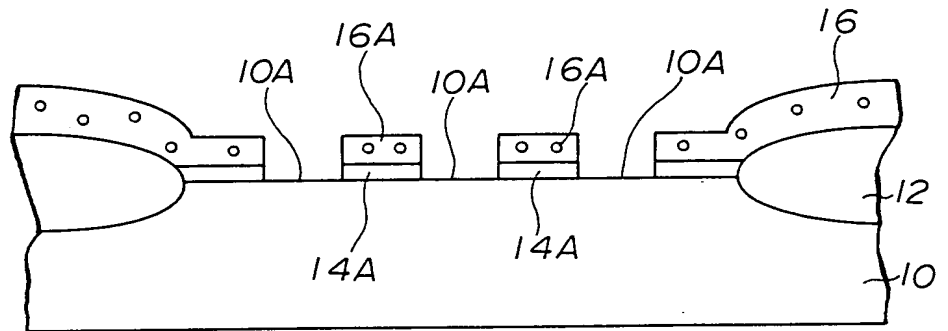


FIG. 2E



MOS REGION

FIG. 2F

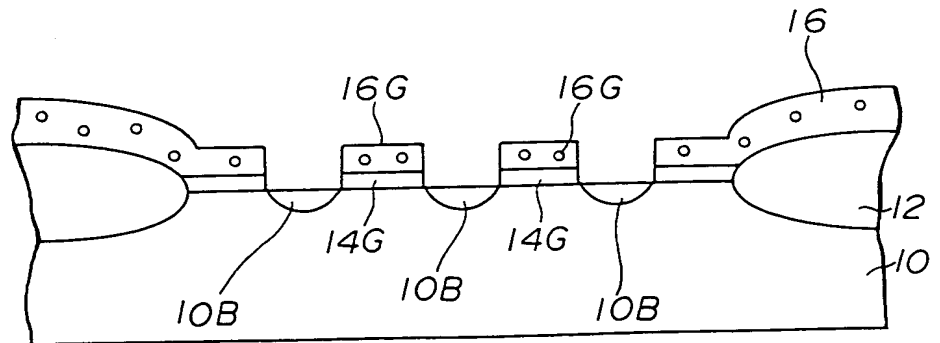


FIG. 2G

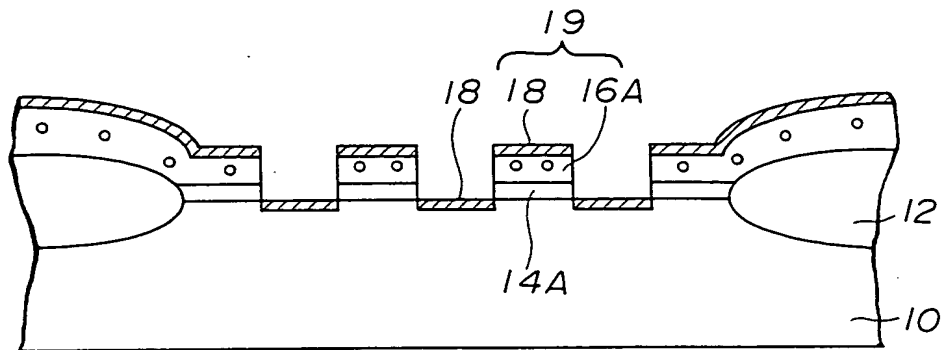


FIG. 2H

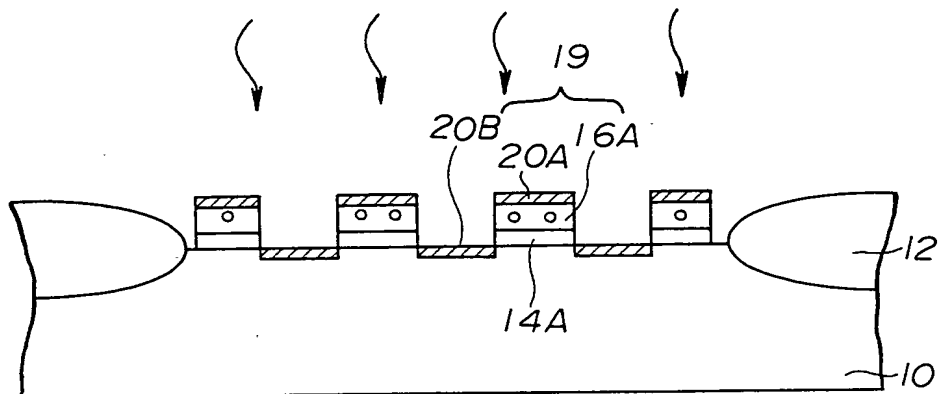
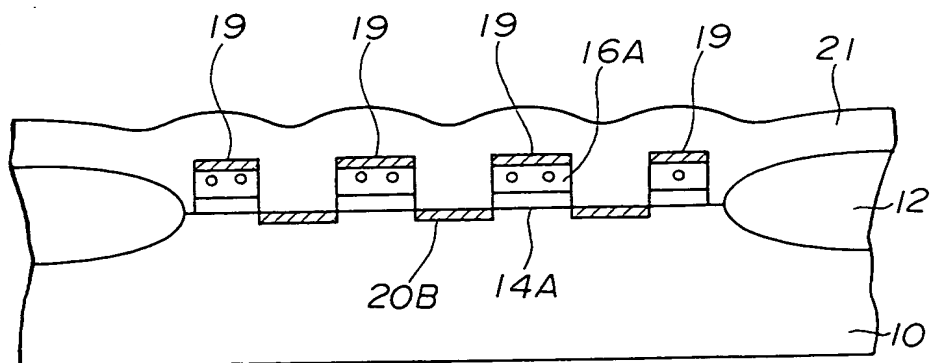


FIG. 2I



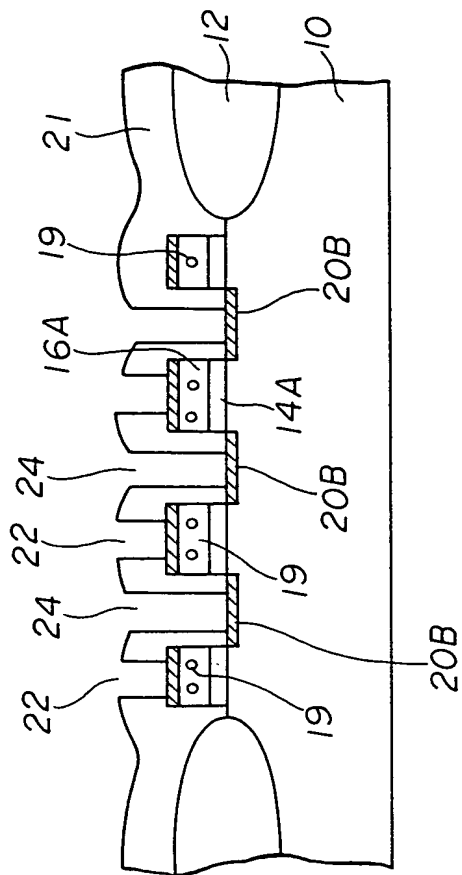


FIG. 2J

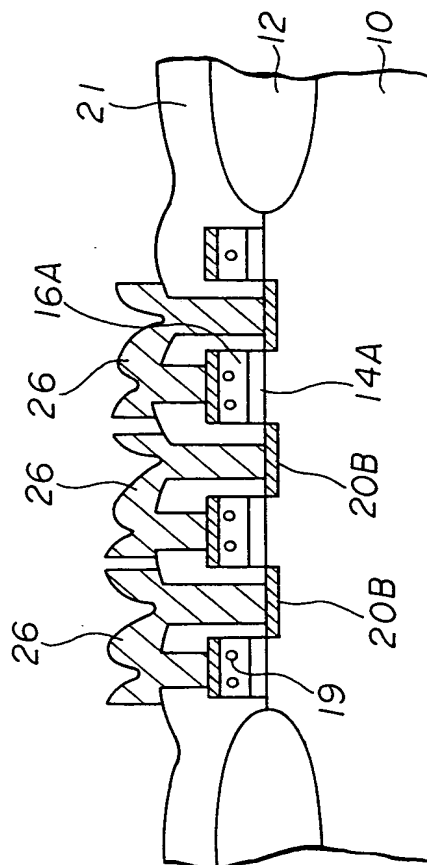


FIG. 2K

FIG. 3

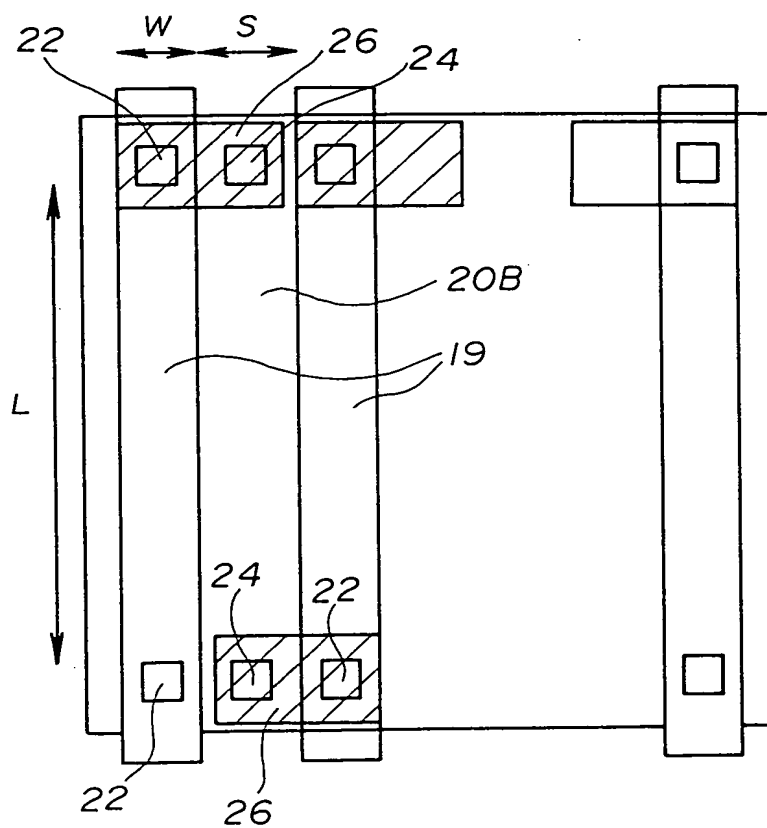
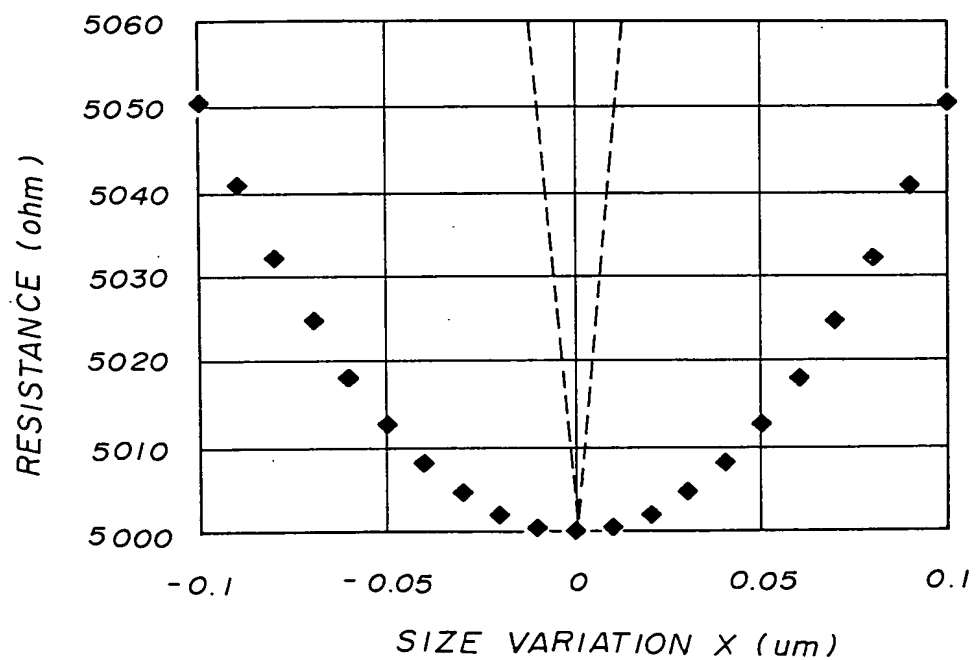


FIG.4



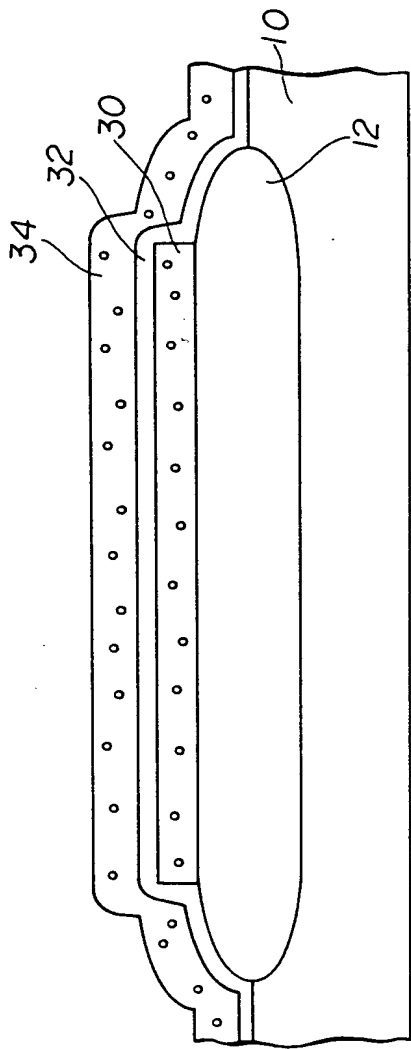


FIG. 5A

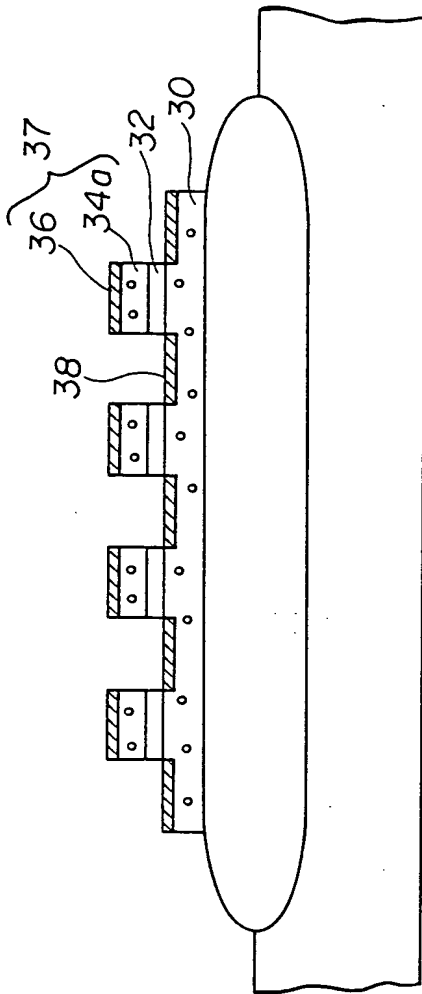


FIG. 5B

FIG. 6

